

FIG.1

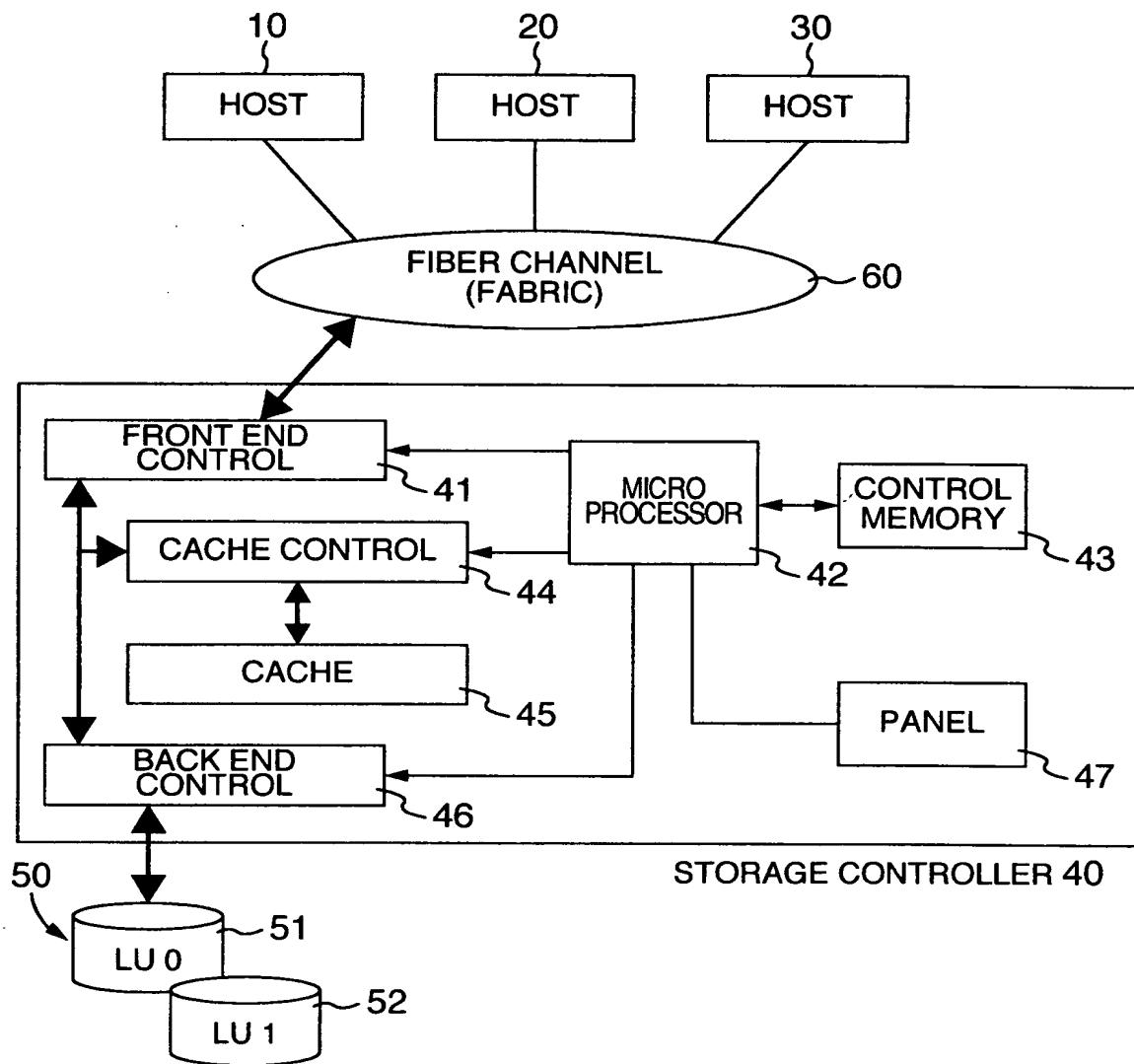


FIG.2

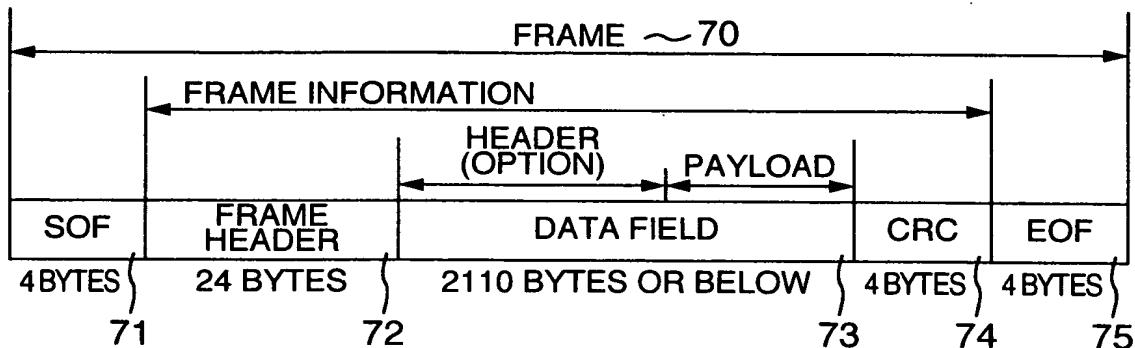


FIG.3

FRAME HEADER \sim 72

WORD \ BIT	31-24	23-16	15-8	7-0	
0	R_CTL	D_ID (N_PORT ADDRESS ID ON RECEIVER SIDE)			~81
1	RESERVED	S_ID (N_PORT ADDRESS ID ON TRANSMITTER SIDE)			~82
2	TYPE		F_CTL		
3	SEQ_ID	DF_CTL		SEQ_CNT	
4		OX_ID		RX_ID	
5			PARAMETER		

FIG.4

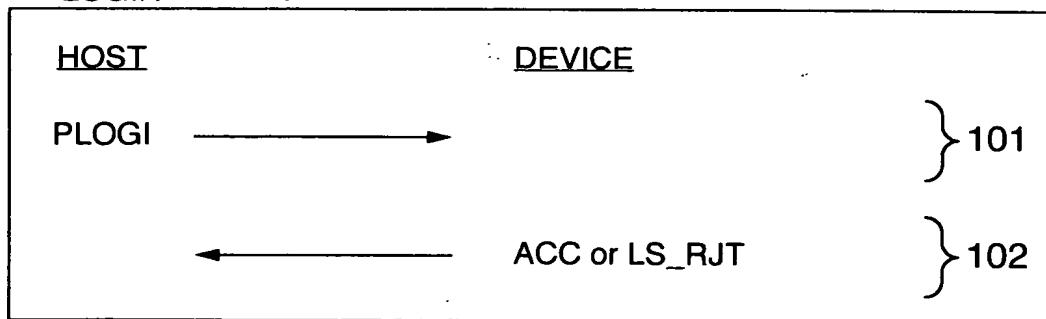
LOGIN \sim 100

FIG.5

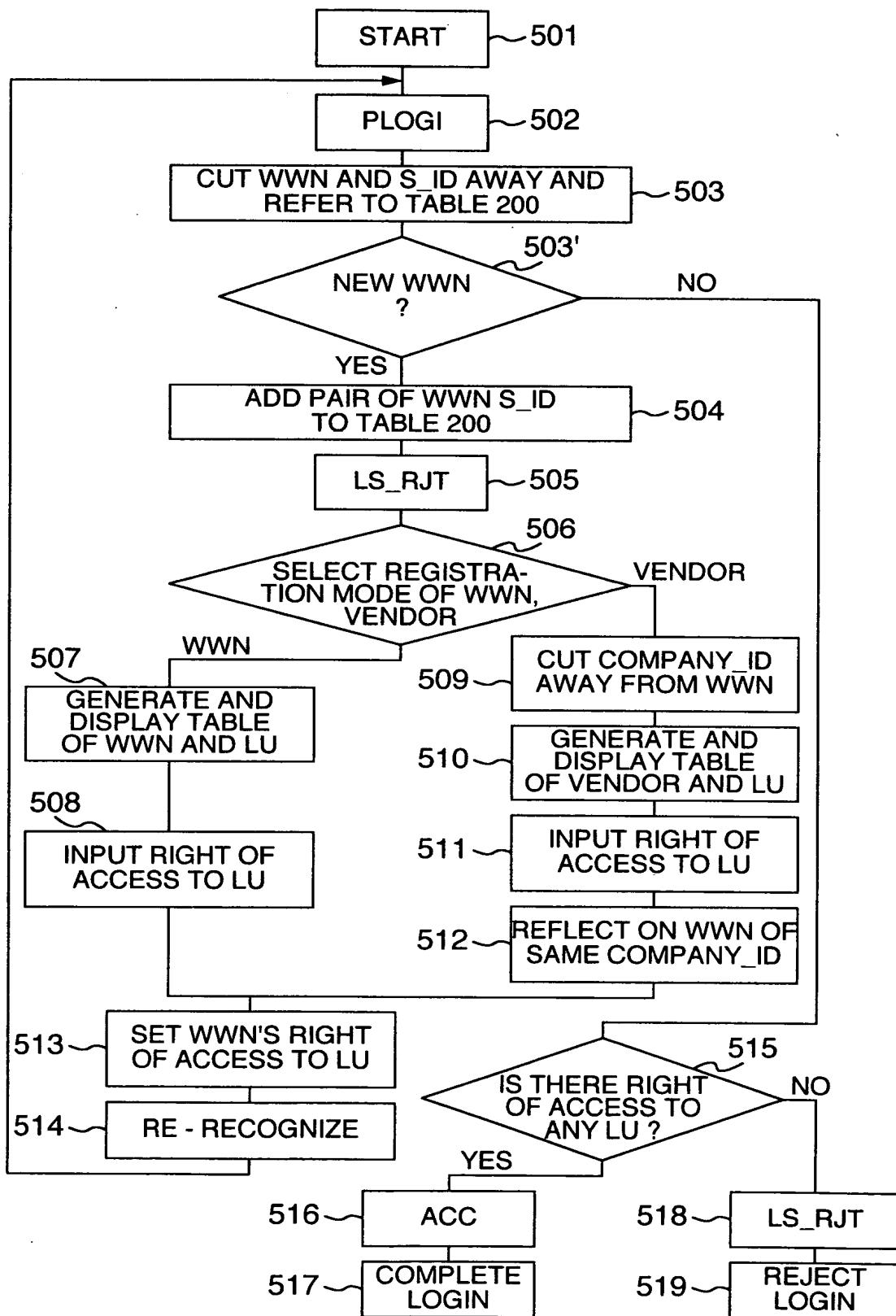


FIG.6

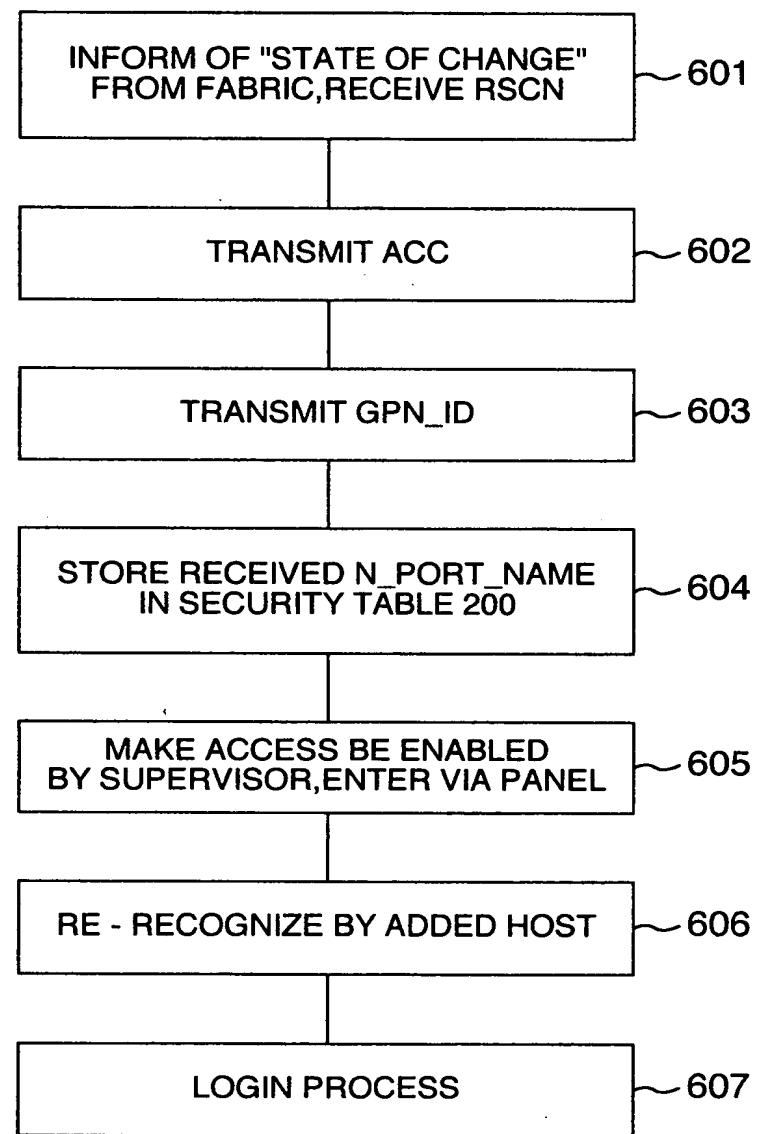


FIG.7A

	WWN	S_ID	200
HOST A	01234567 89ABCDEF	FFFF01	
HOST B	01234567 89ABCDEE	FFFF02	
HOST C	01234567 89ABCDED	FFFF03	

FIG.7B

	ACCESS PERMIT / DENY		201
	LU 0	LU 1	
HOST A	ENABLE	DISABLE	
HOST B	DISABLE	ENABLE	
HOST C	DISABLE	ENABLE	

FIG.7C

	ACCESS PERMIT / DENY		202
	LU 0	LU 1	
VENDOR A	DISABLE	ENABLE	
VENDOR B	ENABLE	DISABLE	

FIG.7D

WWN	S_ID	ACCESS PERMIT / DENY		700
		LU 0	LU 1	
01234567 89ABCDEF	FFFF01	ENABLE	DISABLE	
01234567 89ABCDEE	FFFF02	DISABLE	ENABLE	
01234567 89ABCDED	FFFF03	DISABLE	ENABLE	

FIG.8

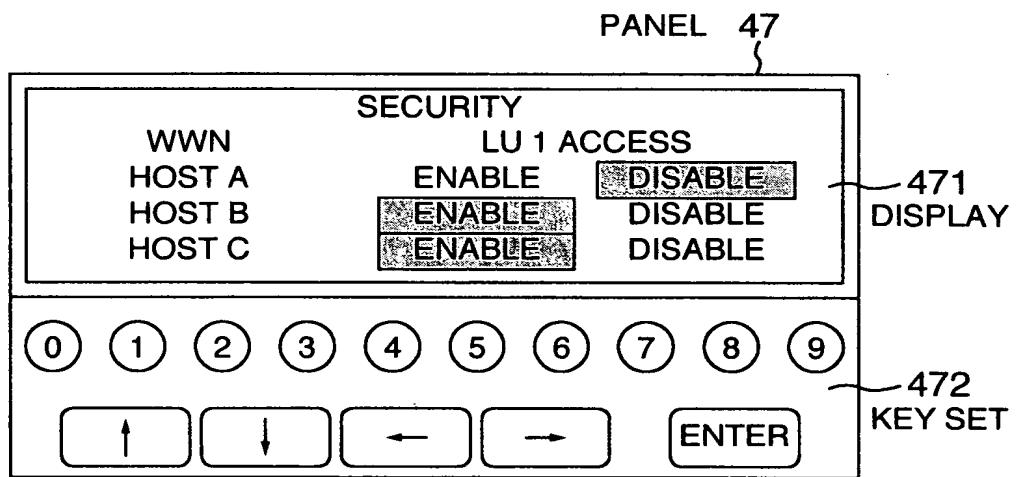


FIG.9

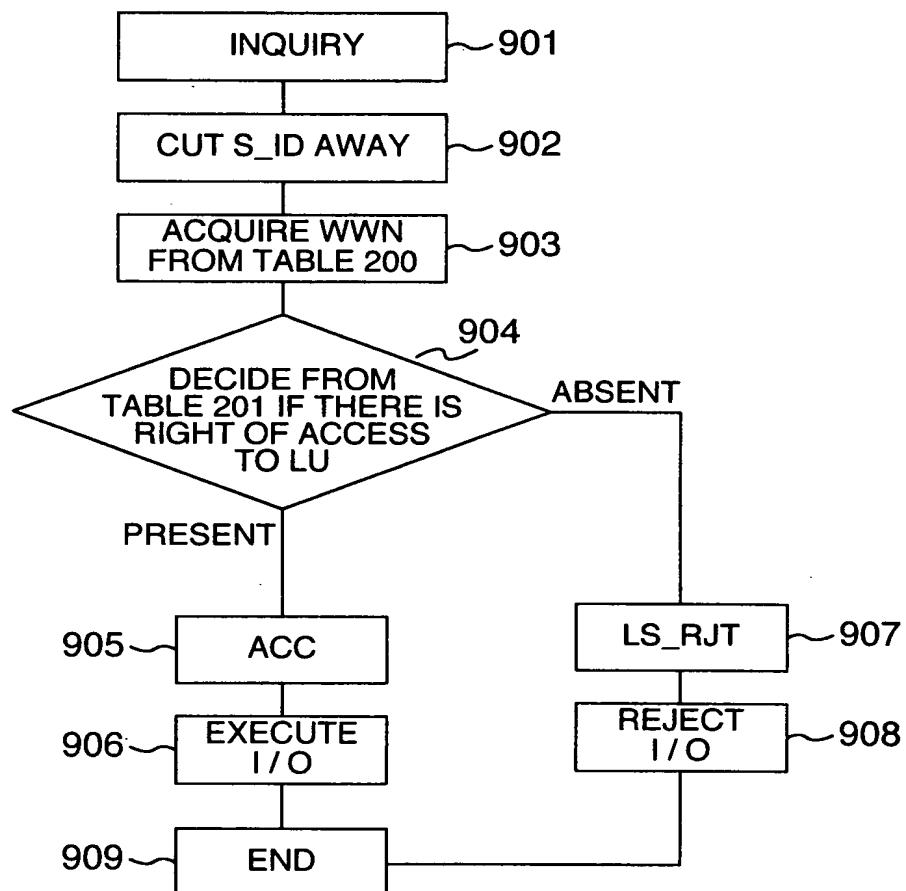


FIG.10

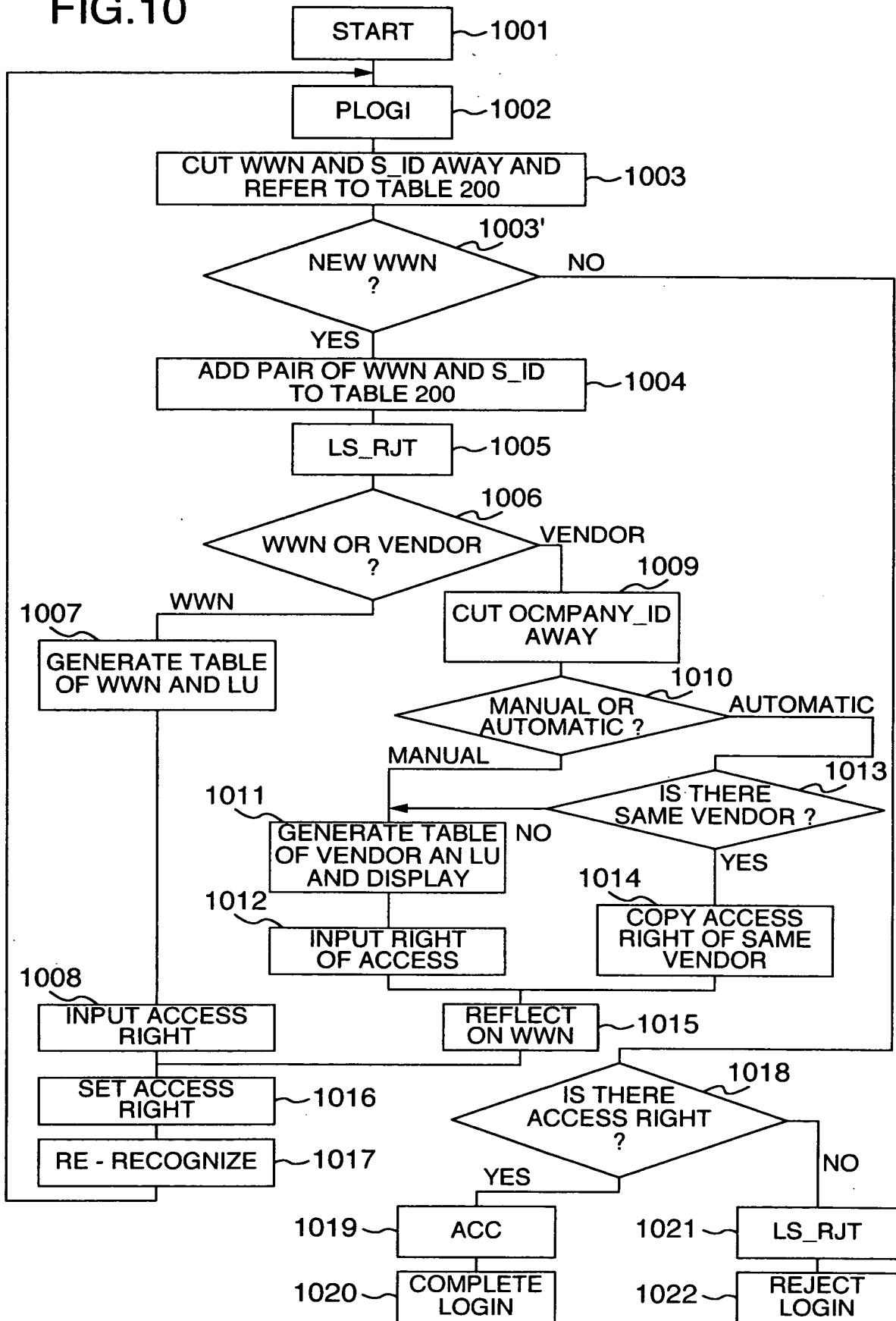


FIG.11

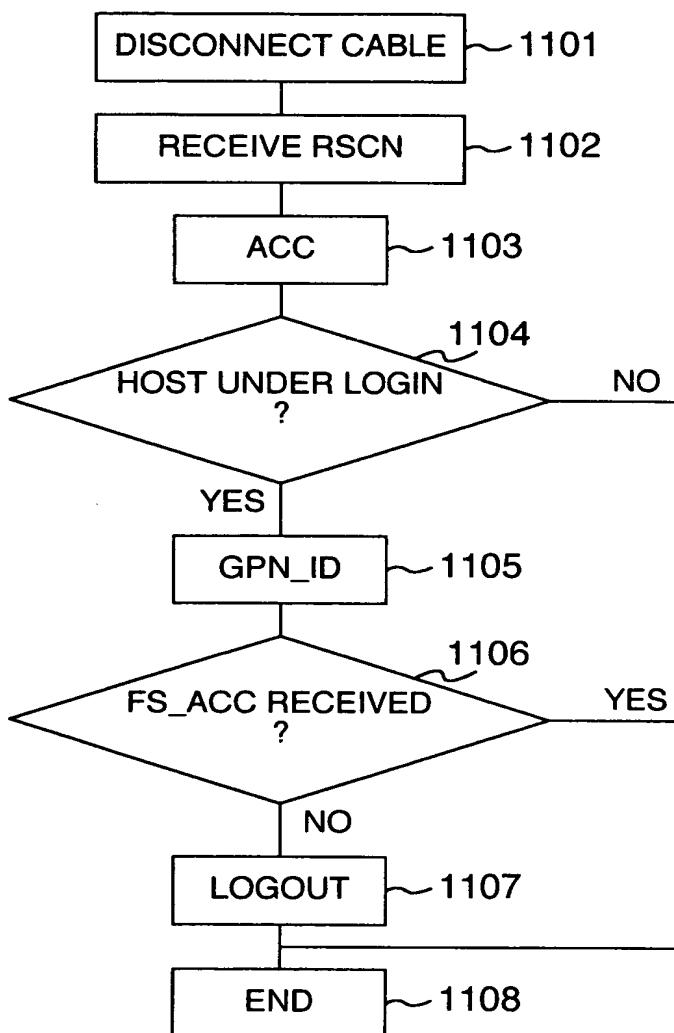


FIG.12

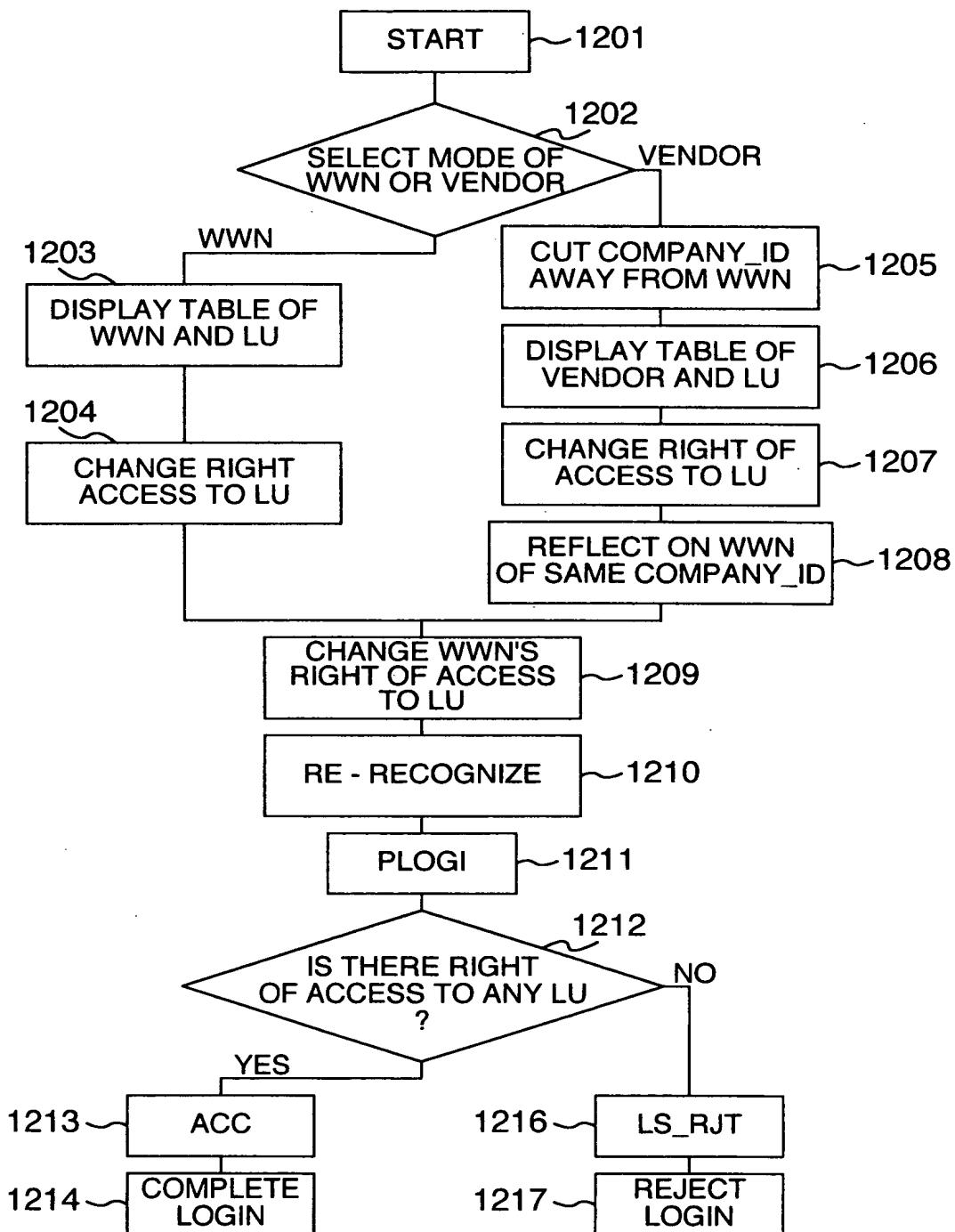


FIG.13

